



## **Francis Balestra**

### SHORT PROFESSIONAL INFO

Francis Balestra was born in Digne, France, in 1960. He received the M.S. and Ph.D. degrees in electronics from the Institut Polytechnique, Grenoble, France, in 1982 and 1985, respectively. In 1989, he obtained the Habilitation diploma from Grenoble INP authorizing him to supervise PhD dissertations.

He joined LPCS (Laboratory of Physics of Semiconductor Devices), Grenoble INP, in 1982, where he has been involved in research on the study, characterization, modeling, and simulation of the first fully depleted and multi-gates silicon-on-insulator MOS transistors.

He became Chargé de Recherche C.N.R.S. (Centre National de la Recherche Scientifique) in 1985 and Directeur de Recherche CNRS in 2000. In 1993-94, he joined the Research Center for Integrated Systems at Hiroshima University as a visiting researcher, and worked on sub-0.1  $\mu\text{m}$  MOSFETs and thin film SIMOX devices.

He led several research teams on Deep submicron CMOS, Silicon On Insulator devices, Ultimate Si-based devices realized with innovating architectures, Low Temperature Electronics and Advanced bipolar transistors for BiCMOS technology. He has supervised 20 research projects and 25 Ph.D.

F. Balestra has been Director of IMEP (Institute of Microelectronics, Electromagnetism and Photonics) and previously from LPCS for a total of 10 years. He is now Director of the Sinano Institute (22 members from 12 European countries) devoted to the coordination of the research activities of the European academic community in the nanoelectronic field. He has coordinated European, National and Regional projects. Within FP6 and FP7, he coordinated the Sinano and Nanosil Networks of Excellence (NoE) that have represented unprecedented collaborations in Europe in the field of nanoelectronics, and is presently coordinating a European NoE of the 7<sup>th</sup> Framework Programme dealing with the study of innovative nanostructures for adding functionalities to CMOS for future nanosystems.

He founded (ULIS, WOLTE) and was the organizer of several international Conferences in the fields of Nanoscale devices, Silicon-On-Insulator structures and Low Temperature Electronics, and many international Workshops and Summer Schools.

He is a member of the European Academy of Sciences, of the Advisory Committee of the Chinese Journal of Semiconductors and Chinese Physics B and received the Blondel Medal (French SEE) in 2001. He is also member of the European ENIAC Scientific Community Council and several ENIAC/AENEAS Working Groups.

F. Balestra has coauthored over 130 publications in international scientific journals, 240 communications at international conferences (more than 70 invited papers and review articles), and 25 books or chapters.